

# ADVANCED POWER CONVERSION PLC APPLICATION NOTES

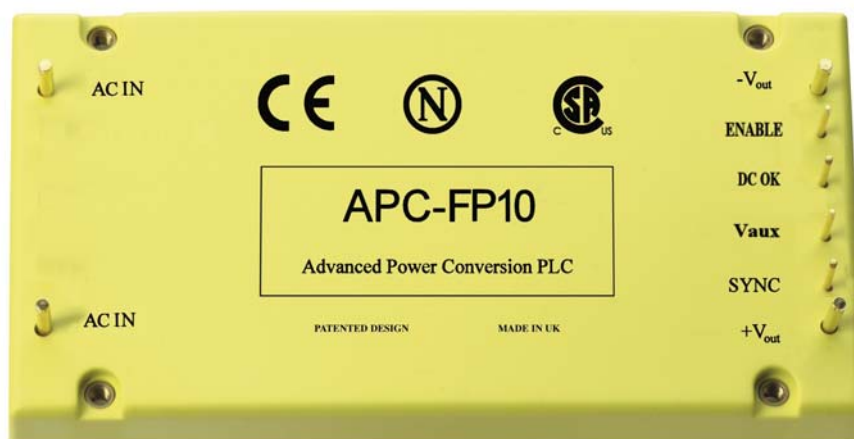
## APC-FP Power Factor Correction Converters

The APC-FP Series of Power Modules are a range of high performance Power Factor Correction AC/DC Converters. The converters have been developed to provide front-end power conditioning for the APC251 High Input Voltage Modules. Wherever used the APC Power Modules are a cost effective alternative to designing power supplies with discrete components.

The APC-FP Series of Power Modules incorporate surface mount technology to achieve high power density and reliability. The converters are available with universal input voltage range for worldwide application or high-line (230Vrms) only, with an output voltage of 380V.

The ENABLE output can be used to control the load DC/DC converters. DCOK and VAUX outputs are available to provide monitoring to the system if required. The modules have output overload protection, inrush current limiting, thermal shut down, output over-voltage and input under-voltage protection.

In addition, a SYNC input pin allows the synchronising of units to an external system clock for particularly noise sensitive applications.



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## Application Notes

### For Power Factor Correction Modules

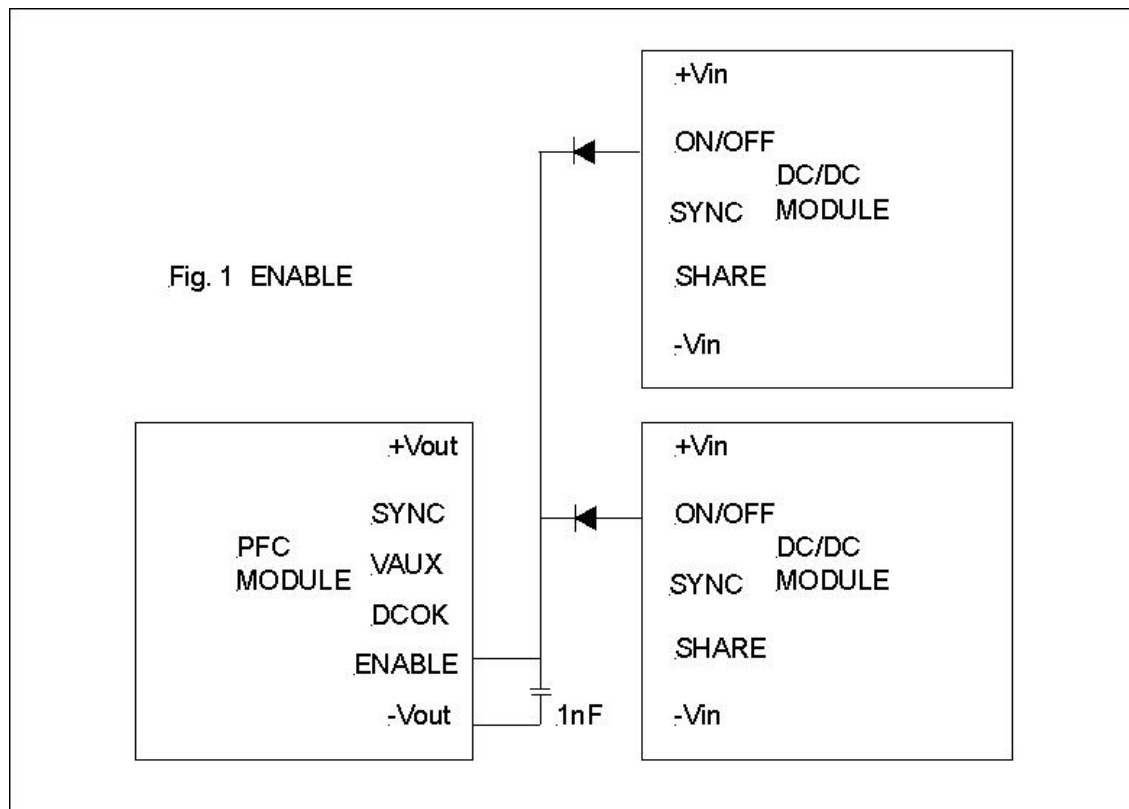
#### ENABLE

The ENABLE output of the Module is an open-drain signal, referenced to  $-V_{out}$ , which is designed to control the load DC/DC converters. The logic is Low = Off. When using an APC High Voltage Input Module as load, all that is required is a single wire connection to its' ON/OFF input. When using more than one load module, connect a small-signal diode from each ON/OFF input to the ENABLE output (see Fig.1).

During start-up, the ENABLE signal will allow the loads to start only when the output has reached 360V (typically). This prevents any current from the loads activating the foldback current limit of the PFC Module, which may cause the system to oscillate. Therefore, APC strongly recommend using this feature.

During shut-down, the ENABLE signal goes low when the output has fallen to 225V (typically). This allows the system to continue working during an input drop-out.

In noisy environments, it may be necessary to add a small ceramic capacitor on the ENABLE output. (A typical value would be 1nF.)



## DCOK AND VAUX

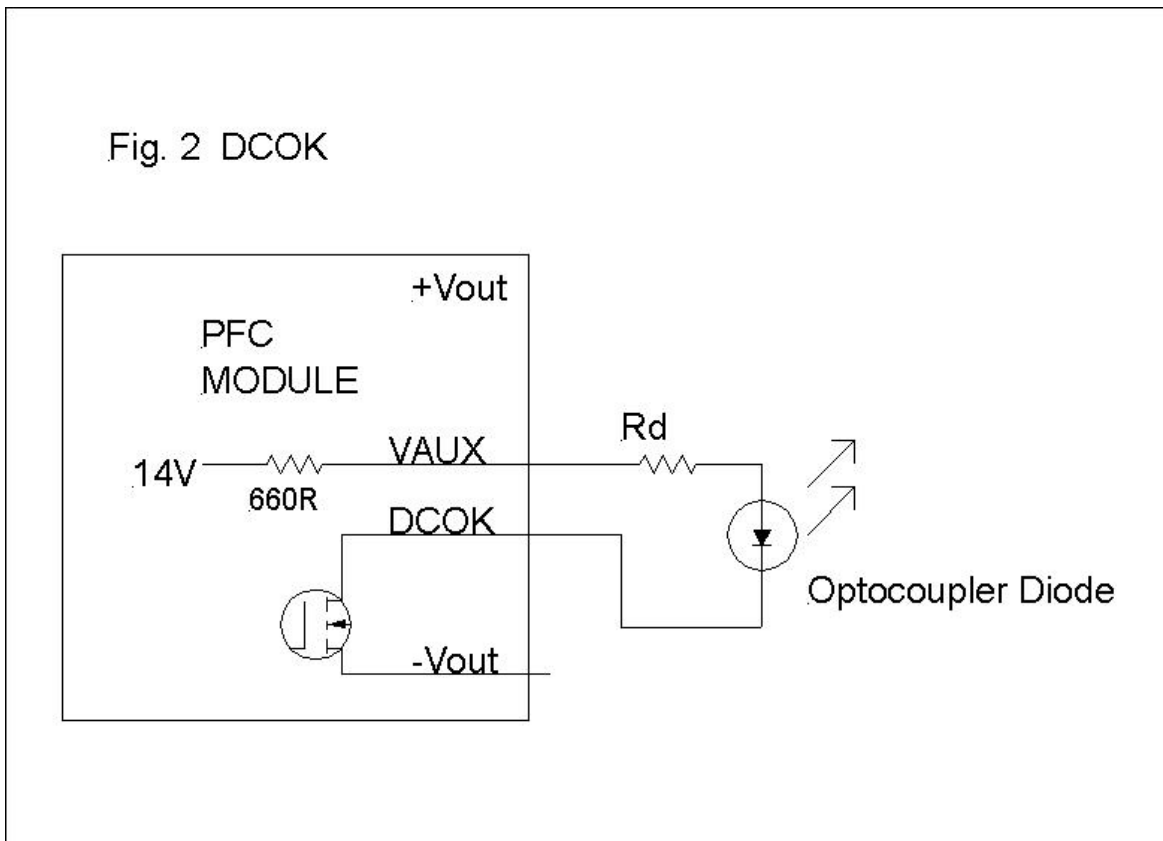
The DCOK output is an open-drain output intended, with the VAUX output, to drive an optocoupler (or similar). Low = Output OK, Open = Output Low. The signal is referenced to the  $-V_{out}$ . The rising threshold is 360V (typically) and the falling threshold is 350V (typically). The VAUX output is a nominal 14.5V supply fed through a 660Ω resistor.

The opto-diode and series resistor  $R_d$  should be connected as shown in Fig. 2. For a desired diode current  $I_d$ ,  $R_d$  can be calculated using the following formula:

$$R_d = (V_{aux} - V_d) / I_d - 660$$

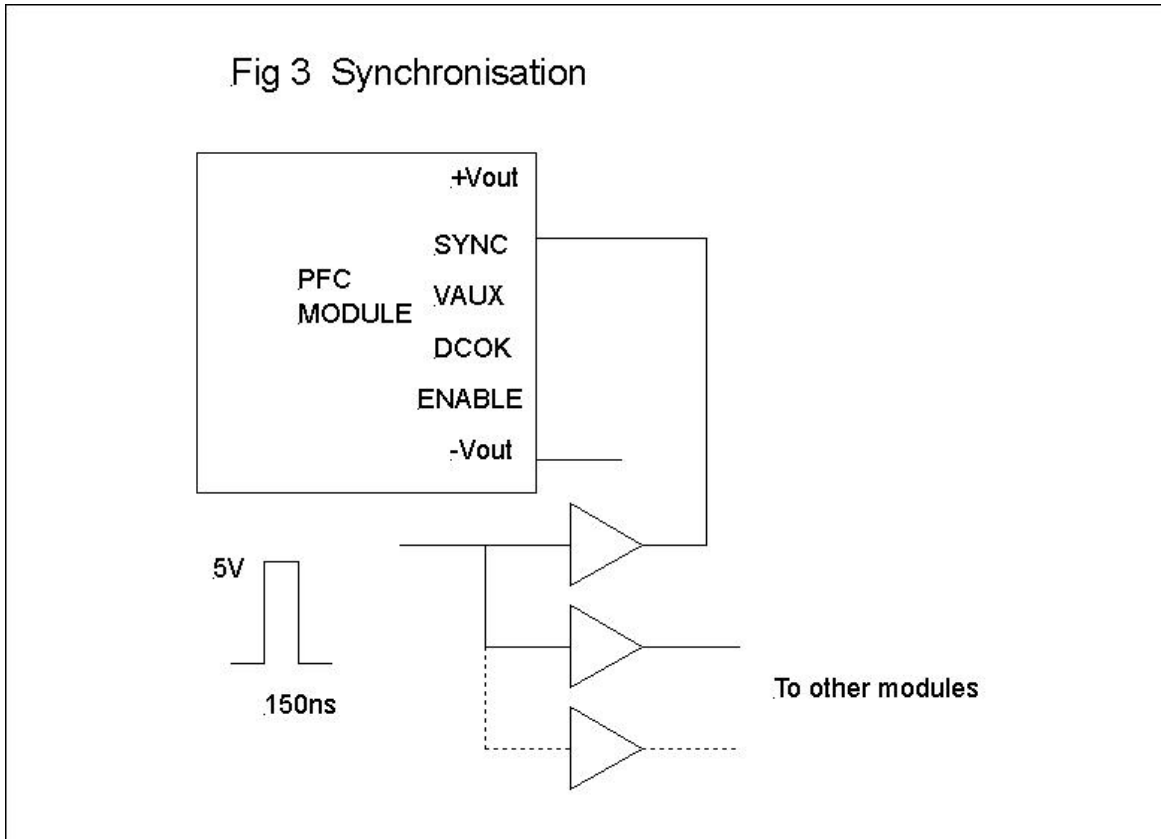
For example, for 10mA current:

$$R_d = (14.5 - 1.2) / 10\text{m} - 660 = 670\Omega$$



## **SYNCHRONISATION**

When used for particularly noise sensitive applications one or more Power Modules can be synchronised to an external clock frequency. The external 5V TTL compatible driving signal drives the SYNC pin of the Power Module directly.



Synchronisation pulses are nominally +5v and 150nSec long ( $\pm 50$ nSec), with a pulse repetition frequency (p.r.f.) selected to be between 165 kHz and 180kHz. Higher switching frequencies can be accommodated if required. APC application engineers are available to give advice on special requirements.

The input impedance is a resistive  $90\Omega$  to ground. There is negligible capacitive loading; the external stray pcb capacitance from the external drive to the Power Module is typically dominant and having a low duty cycle, (typically 5%), minimizes the drive power requirements to achieve synchronisation in a very efficient manner.

Typical of the devices to drive the synchronising pulse to the Power Modules are FET drivers supplied from a 5V power rail, a suggested example being the widely used 426 series.

Note: the 0V reference ground for the SYNC function is the -Vout terminal of the module

## **SAFETY CONSIDERATIONS**

The modules are intended for use within suitable enclosures and safety must be considered in positioning the modules.

- ◆ In order to give the designer flexibility with regard to grounding, the metal base plate of the power modules is isolated from the internal circuitry by single (“Basic”) isolation. In Class I applications, where a safety ground is provided, the base plate must be connected to the safety ground. In Class II applications where no ground is provided, the base plate and any conductive parts to which it is connected must be isolated so they do not present a shock hazard in the event of insulation failure between the input circuitry and the base plate (giving double isolation).
- ◆ To ease grounding, the fixings are connected to the base plate and can be used to connect the base to ground.
- ◆ To prevent risk of fire in case of certain kinds of fault developing within the converter, the input circuit should be fitted with appropriately rated fuse.
- ◆ The modules are designed to operate at temperatures that may be hazardous if touched. Care should be taken to prevent touching of metal parts.
- ◆ Modules may have a voltage across the Vin and Vout terminals after removing the power. The voltage, stored as a charge on capacitors, may not decay to low levels immediately. Users should take this into account when gaining access to the modules within the equipment.

## **FUSES**

An external input fuse is needed for electrical safety (and to protect other circuits) in the unlikely event of catastrophic failure of a Power Module.

This fuse should be selected to withstand the maximum input current at low input voltage. However fuse selection should take into account that fuses de-rate with time and may therefore open under normal conditions. A typical fuse for a PFC module driving two 200W modules, from a 230Vac input, would be rated at 3A. Time-delay (T) type fuses are recommended to prevent blowing during inrush transients.

## **OVERLOAD PROTECTION**

The PFC Modules have an output current limit circuit for overload and inrush/start-up. This circuit is in series with the positive output of the module. Since there is a large capacitor on the output, this circuit limits the charging current and therefore the inrush current, during start-up.

The current limit circuit has a foldback characteristic to prevent excessive dissipation in the series element as it charges the load capacitance. Due to the AC nature of the input, this means that the charging/inrush current varies as the input voltage changes.

The foldback characteristic means that the unit may not start into a resistive load. To start into a resistive load, ensure the resistance is greater than 25 ohms. Higher loads may then be applied once the unit is running.

During an overload condition to the output, the series protection circuit limits the current. When the overload is removed the Power Module automatically returns to normal operation.

A sudden application of a short circuit on the output of the module, when running, may cause failure of the module. It is therefore recommended to fuse the inputs of the load converters.

## **INPUT UNDERVOLTAGE SHUTDOWN**

The PFC Module is protected against input undervoltage (brown-out) using a detector that monitors the peak input voltage. This is to prevent the module overheating in the unlikely event of the input voltage falling below the normal range.

## **OUTPUT OVER VOLTAGE PROTECTION**

The output is also protected against over voltage by an internal over voltage protection circuit that shuts down the switching for the duration of the excessive voltage. The threshold is typically 415V.

Over voltage can occur during:

1. A major failure of the control circuit. In the unlikely event of this happening the PFC Module has to be replaced.

2. Start-up and large load changes, due to the (necessary) slow nature of the feedback circuit of the Module.
3. Input Over-Voltage (if the peak of the input exceeds the threshold).

## **DECOUPLING**

The APC PFC Modules have built in ceramic capacitors to absorb high frequency noise, but it is also necessary to add local input and output decoupling capacitors. A ceramic or plastic capacitor, of at least 400V rating, should be connected close to the input pins of the module; typically 1 $\mu$ F is sufficient. An electrolytic capacitor, of at least 450V rating, should be connected close to the output pins of the module; at least 100 $\mu$ F is recommended. This capacitor also absorbs the twice-line-frequency ripple, the input ripple of the DC/DC converter and provides hold-up for mains drop-outs. For 20ms hold-up, 1 $\mu$ F per Watt of load is recommended.

## **THERMAL PROTECTION**

A common failure mode in a power supply is overheating which may have been caused by inadequate thermal design, too high ambient temperature or failing forced cooling, sometimes caused by dirty air filters. Thus thermal protection and management is of prime importance for the reliability of a power supply.

The Power Modules are designed for mounting directly on to a heatsink with the flat metal base acting as the primary means of removing heat. Suitable heatsinks are available from APC or other standard heatsinks can also be used. For good thermal contact between the Power Module's metal base and the heatsink a thermal pad should be used.

To obtain long life and a reliable performance the temperature rise within any power device cannot exceed certain values. For the APC PFC Modules, the temperature of the metal base has to be controlled so that the maximum continuous temperature is 100°C.

## **HEATSINK OPERATION**

Some form of heatsinking is necessary to use the maximum output of the Power Modules and if space is not at a premium natural convection is normally used. The size and type of heatsink is determined by the available mounting space, ambient temperature, mounting orientation and output power requirements. Many standard heatsinks are available and sometimes the enclosure within which the power supply is located can be used for this purpose.

APC can supply a specially designed heatsink for Power Modules mounted on a printed circuit board. This heatsink has a thermal resistance of  $2.8^{\circ}\text{C}/\text{W}$  when mounted with the fins vertically. For maximum output power from the Power Module a fan is required to cool the assembly. The flow rate is dependent upon the required output power of the module and the maximum ambient air temperature. For high ambient temperatures a flow rate of up to 5m/s should suit most applications, reducing the thermal resistance to  $0.6^{\circ}\text{C}/\text{W}$

The ideal thermal resistance for full operation without a fan is  $0.5^{\circ}\text{C}/\text{W}$ .

The metal base of the Power Modules is thermally coupled to the heat sources within the Power Module but is electrically isolated from its circuitry. It is very important that the mating surfaces of the heatsink and the Power Module are flat. The metal base of the Power Modules has a flatness of 0.2mm for this purpose.

Thermally conductive material should also be used to fill the voids between the mating surfaces. APC recommends the use of a dry or phase change material. The thermal resistance between the two surfaces should be typically  $0.1^{\circ}\text{C}/\text{W}$  for materials obtained from most manufacturers.

Silicon compounds are greasy and have a tendency to creep along the surface of the heatsink with time. Other new "phase change" materials are designed to replace grease as a thermal interface. The phase change temperature are typically between  $55^{\circ}\text{C}$  to  $65^{\circ}\text{C}$  at which point the material changes from a solid to a thixotropic characteristic assuring total wet-out of the two surfaces reducing still further the thermal resistance.

Examples of dry thermally conducting material are Union Carbide "Grafoil", Warth "KA150" and Bergquist "Hi Flow 105" for phase change materials, all are for non-isolated applications.

## **EMI FILTERING**

Like all power switching circuits, the modules generate high frequency noise in both series-mode and common-mode. In order to meet regulatory requirements, it is necessary to add an EMI filter between the module and the input line. This filter would typically have to be a dual-stage type. For further details see Application Note: Off-Line EMC Filtering.

It is normally necessary to include ceramic capacitors (e.g. 4.7nF, Y-type) from the –Vout (or +Vout) pin, and the AC input pins, to the base/heatsink/ground. They should be placed as close as possible to the module, with low impedance paths to the pins and the base.

## **NOTES**

Information and specifications are believed to be correct at time of publication and are subject to change without notice. APC accepts no responsibility for consequences arising from printing errors or inaccuracies.

## TYPICAL APPLICATION CIRCUITS

